

Appln No. 10/760,227
Amdt. Dated November 17, 2006
Response to Office Action of August 22, 2006

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REMARKS/ARGUMENTS

In response to the Examiner's further Office Action of August 22, 2006 issued with respect to the present RCE application, the Applicant respectfully submits the accompanying Amendment to the claims and the below Remarks.

Regarding Amendment

In the Amendment:

independent claim 1 is further amended to specify that a single controller is mounted by the support frame for processing print data and controlling all of the printhead integrated circuits to print the processed print data. Support for this amendment can be found at page 15, line 28-page 16, line 8 and page 16, lines 11-3 of the present specification; and

dependent claims 2, 3, and 5-10 are unchanged.

It is respectfully submitted that the above amendments do not add new matter to the present application.

Regarding 35 USC 102(b) Rejections

It is respectfully submitted that the subject matter of amended independent claim 1, and claims 2, 3 and 5-10 dependent therefrom, is not disclosed by Silverbrook, for at least the following reasons.

In light of the Examiner's continued interpretation of the shift register, transfer register, enable gate, drive transistor, data connections, and power connections disclosed by Silverbrook as purportedly satisfying the claimed controller and connector, independent claim 1 has been further amended as described above to limit the claimed invention to an arrangement in which a single controller is mounted to the support frame for processing print data for and controlling each of the printhead ICs of the printhead module.

As described at page 15, line 28-page 16, line 8 and page 16, lines 11-3 of the present specification and as discussed by the Applicant in the previous remarks of 2/8/06 (mentioned by the Examiner in the Response to Arguments section of the present Office Action), this arrangement allows easy replacement and selection of multiple modules of printhead ICs without the need to provide individual controllers and connections for each printhead IC.

On the other hand, the above-mentioned components of Silverbrook cited by the Examiner, i.e., shift register, transfer register, enable gate, drive transistor, data connections, and power connections, are clearly disclosed by Silverbrook as being individually provided for each printhead chip 18 (see col. 3, lines 45-50 and col. 4, lines 6-18, for example).

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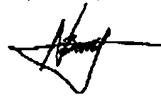
Thus, the subject matter of amended independent claim 1, and claims 2, 3 and 5-10 dependent therefrom, is neither disclosed nor suggested by Silverbrook.

It is respectfully submitted that all of the Examiner's rejections have been traversed. Accordingly, it is submitted that the present application is in condition for allowance and reconsideration of the present application is respectfully requested.

Very respectfully,

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